

XANVision



Define Requirements

Effective test design for compliance verification is a complex task. XANVision simplifies the process by enabling a QA engineer to easily identify, record, and sort requirements from a design document. A QA engineer gathers test requirements by using point and click operations. One is able to capture, categorize, and refine requirements during a simple overview of highlighted requirement statements that XANVision produces. These requirement statements captured by XANVision are then converted into XML and tagged with the location within the design document. Searching, sorting and organizational capabilities identify omissions and contradictions that might be present in the design document. XANVision generates test documentation to help guide QA engineers in the creation of test procedures.

Increase Efficiency

XANVision's ability to identify duplicates, categorize by keywords, and add additional attributes enable engineers to optimally group conformance items. The grouping of these tests significantly reduces the number of tests that are needed for a set of requirements. This saves time and money while bringing structure and consistency to a difficult process.

Guarantee Results

XANVision is a critical piece of the XANSation Enterprise Test System to ensure that test documentation created early on can be efficiently reused and updated throughout the product development cycle.

XANVision Module Benefits

- Fosters early detection of specification errors
- Ability to search through a list of conformance statements and identify requirements
- Reduces the overall effort associated with implementing a set of requirements, while bringing structure and efficiency to the process
- Point and click creation of testable items list
- Optimally group conformance items into test procedures
- Point and click location of requirements in a design document
- Ability to generate test documentation in PDF and XML formats
- Search lists of conformance items by key words
- Identify changes to conformance items

- Matches like items to prevent duplication of effort



Platform

XANVision has been verified under Windows XP & Vista platforms.

For more information contact:

Sales at LNI

Phone 603-868-8411
Sales@lampreynetworks.com



XANVision simplifies and accelerates the process of translating design documents into a set of testable conformance items

Specification Document Information

title	draft-ietf-rddp-mpa-08.pdf
version	0.3
format	pdf
localPath	iWARP TestSpec.pdf
GlobalURL	
fieldDelimiter	::

Properties

Compliance Statement

displayName	MPA.5.1-2
UUID	-47c3-b0e9-6b19ecedd0e5
previousDisplayName	MPA.3.1.1-2
state	DONE
summary	Multiple FPDUs MAY be packed into a single TCP segment
fullText	Multiple FPDUs MAY be packed into a single TCP segment as determined by the EHSS calculation as
implementable	true
matchKeys	MPA, TCP, FPOU, SEGMENT
specRef	
inDocRef	pdfDocRef
pdfDocRef	
tagDocName	draft-ietf-rddp-mpa-08
tagPageStart	23
tagIndexStart	276
tagName	
tagPageEnd	23
tagIndexEnd	302
tagType	
tagLevel	0
pdfDocRef	none
tagDocName	
tagPageStart	0
tagIndexStart	0
htmlDocRef	
implementation	
toBeImplemented	true
reason	none

Screen 1: XANVision displaying iWARP Conformance Guidelines. (Left)

Screen 2: XANVision displaying iWARP Test Document. (Below)

Title: SIMPLE BASIC OP - SEND - DUT AS SINK

Core Name: C07_01_01_01

Developer Notes:

Name	Date	Note
1. Author	5/23/2008	Initial Test Description

Guideline Coverage:

Display Name	Relative Path	UUID
1. DDP.4.1-3:1a:1	tdMap.xml	762-4462-90e9-22b19c741c3a
2. RDMAP.4-2:1a:1	tdMap.xml	4e2a-49af-6c3b-75d6d9f90cc38
3. RDMAP.5.3-3:1a:1	tdMap.xml	52b-4d51-819a-848fa238a7
4. RDMAP.5.3-3:1b:1	tdMap.xml	02b-4d51-819a-848fa238a7

Test Category: iWARP

Purpose:
To verify that a DUT can correctly receive a SEND message.

Discussion:
A SEND operation uses untagged buffers, as such the 1st bit of the DDP Control Field must be set to '0'. The RDMAP layer is handed the message length by the DDP layer. The LUP Message sent using a Send Message Type MAY be less than or equal to the size of the consumed Untagged Buffer. Since the interface between the DDP and RDMAP layer on the DUT is not visible, the test station infers that the correct message length is passed to the RDMAP layer by comparing the number of bytes returned in the LUP_SEND_RECV message to the number of bytes placed in the RDMAP SEND message generated by the test station.

Configuration:
[Configuration A. Simple Link.xml](#)

Tester Procedure:

- Passive Initialization Procedure**
Remark: To control the size of the DUT's untagged buffers, the post buffer mode on the DUT is set to manual.
- Tester Issues an RDMA Send message to the DUT that is smaller than buffer on DUT
- RDMAP.5.3-3:1a:1** Verify the RDMA Send message was correctly written to an Untagged Buffer.
- Tester issues a Send message to the DUT that is the same size as the untagged buffer on DUT.
- RDMAP.5.3-3:1b:1** Verify the RDMA Send message was correctly written to an Untagged Buffer.
- RDMAP.4-2:1a:1** Verify that the DDP Layer passes the correct message length to the RDMAP Layer for both RDMA send operations.
- DDP.4.1-3:1a:1** Verify that the DDP Layer passes the correct message length to the RDMAP Layer for both RDMA send operations.

- DUT that is smaller than buffer on DUT
- RDMAP.5.3-3:1a:1** Verify the RDMA Send message was correctly written to an Untagged Buffer.
 - Tester issues a Send message to the DUT that is the same size as the untagged buffer on DUT.
 - RDMAP.5.3-3:1b:1** Verify the RDMA Send message was correctly written to an Untagged Buffer.
 - RDMAP.4-2:1a:1** Verify that the DDP Layer passes the correct message length to the RDMAP Layer for both RDMA send operations.
 - DDP.4.1-3:1a:1** Verify that the DDP Layer passes the correct message length to the RDMAP Layer for both RDMA send operations.



Making Verification A Competitive Advantage

