

InfiniBand™ Core Test List

(Tests for Switch only)

Addressing, Link and Network

Test Number	Test Subsection	Description
C4-001	C04-001 & 02	Default GUID Check
C7-001	C07-01-01	Normal Link Init to Active Path
	C07-01-02	Link Active State Test
	C07-01-03	Link ActiveDefer State Test
	C07-01-04	Link Arm State Test
	C07-01-05	Link Initialize State Test
C7-007	C07-07-01	Packet with Bad VCRC
	C07-07-02	Packet with Bad ICRC
	C07-07-03	Packet with bad LVer
	C07-07-04	Packet with Bad PktLen
	C07-07-05	Packet with bad DLID
	C07-07-06	Packet with Bad VL
	C07-07-07	Packet with GRH and VL15
	C07-07-08	Buffer Overflow
	C07-07-09	Bad Pkt
	C07-07-10	Marked Bad Packet
C7-029	C07-29-01	MaxVL Check
	C07-29-02	Less than Max VL Check
	C07-29-03	Illegal Operational VL
C7-030	C07-030-01	Link Initialize Send Link Packets Check
	C07-030-02	Link Arm Send Link Packets Check
	C07-030-03	Link Active Send Link Packets Check
	C07-030-04	Link Initialize Receive Link Packets Check
	C07-030-05	Link Arm Receive Link Packets Check
	C07-030-06	Link Active Receive Link Packets Check
C7-031	C07-031	Minimum Buffer Resources per VL
C7-032	C07-032	Data Packets Shall Be Placed In The Associated VI
C7-040	C07-040-01	Verifies LNH field for Local Packets
	C07-040-02	Verifies LNH field for Global Packets
C7-046	C07-046	SLID Assignment Check
C7-053	C07-53-01	Flow Control - Op Link Initialize
	C07-53-02	Flow Control - Op Link Arm
	C07-53-03	Flow Control - Op Link Active
C7-058	C07-058	Flow Control - FCTBS - Initialization
C7-062	C07-062	Flow Control - Verify values as FCTBS accumulates
C7-069	C07-069-01	Error Detection - Initialize
	C07-069-02	Error Detection - Armed
	C07-069-03	Error Detection - Active
C7-070	C07-070#02	Control Update Watch Dog - ARM
	C07-070#03	Control Update Watch Dog - Active
C8-006	C08-006	GRH Rule for PayLen Value
C8-010	C08-010 & 011	GRH Rule for SGID Value

Management

Test Number	Test Subsection	Description
C13-013	C13-013	Response Time Values
C13-014#01	C13-014#01	Response Time Value Single Packet
C13-014#02	C13-014#02	RespTimeValue Non Subn & Non Dir Route
C13-026	C13-026	Attribute Modifier not used
C13-045.1.1 MAD Validation	C13-045.1.1	MAD Validation - Part 1
	C13-045.1.1	MAD Validation - Part 2
	C13-045.1.1	MAD Validation - Part 3
	C13-045.1.1	MAD Validation - Part 4
	C13-045.1.1	MAD Validation - Part 5
	C13-045.1.1	MAD Validation - Part 6
	C13-045.1.1	MAD Validation - Part 7
	C13-045.1.1	MAD Validation - Part 8
	C13-045.1.1	MAD Validation - Part 9

Subnet Management

Test Number	Test Subsection	Description
C14-010	C14-010	Directed Route SMP Field Initialization
C14-015	C14-015	No M Key Checking
C14-016_Get	C14-016_Get	M Key Checking for SubnGet
C14-016_Set	C14-016_Set	M Key Checking for SubnSet
C14-016_Trap	C14-016_Trap	M Key Checking for SubnTrapRepress
C14-017	C14-017	M key Lease Period Timer
C14-018	C14-018	M Key Violation counter
C14-023	C14-023	M KEY Components in NVRAM
C14-024#02	C14-024#02	NodeDescription
C14-024#03	C14-024#03	NodeInfo
C14-024#04 SwitchInfo	C14-024#04-01	SwitchInfo - Read Only Components
	C14-024#04-02	SwitchInfo - Read Write Components Part 1
	C14-024#04-03	SwitchInfo - Read Write Components Part 2
C14-024#05	C14-024#05	GUIDInfo
C14-024#06 PortInfo Switch	C14-024#06-01	PortInfo for Switch only - Part 1
	C14-024#06-02	PortInfo for Switch only - Part 2
	C14-024#06-03	PortInfo for Switch only - Part 3
	C14-024#06-04	PortInfo for Switch only - Part 4
	C14-024#06-05	PortInfo for Switch only - Part 5
	C14-024#06-06	PortInfo for Switch only - Part 6
	C14-024#06-07	PortInfo for Switch only - Part 7
C14-024.1.1#06	C14-024.1.1#06	PortInfo LocalPortNum
C14-024#07 P_Key	C14-024#07-02	Partition Key Table for Switch Only - Unsupported Attribute
	C14-024#07-03	Partition Key Table for Switch Only - Supported Attribute - Part 1
	C14-024#07-04	Partition Key Table for Switch Only - Supported Attribute - Part 2
	C14-024#07-05	Partition Key Table for Switch Only - Supported Attribute - Part 3
C14-024#08 SL2VL Switch	C14-024#08-03	SLToVLMMapping Table for Switches Only
	C14-024#08-04	SLtoVLMMapping Table - Switch - Unsupported
	C14-024#08-05	SLtoVLMMapping Table - Switch - Supported
C14-024#09 VLArb	C14-024#09	VLArbitrationTable - Switch
C14-024#10 LFT	C14-024#10-01	LinearForwardingTable - Supported - Invalid Ports
	C14-024#10-02	LinearForwardingTable - Supported - Valid Ports
	C14-024#10-03	LinearForwardingTable - Unsupported
C14-024#11	C14-024#11	RandomForwardingTable
C14-024#12	C14-024#12	MulticastForwardingTable
C14-024#15	C14-024#15	LedInfo

Physical Layer - Device

Test Number	Test Subsection	Description
V2c06-007#2	V2c06-007#2	Differential Output Peak-Peak Voltage - 1.6V Max
V2c06-007#5	V2c06-007#5	Rise Time Minimum - 100 ps (mean time)
V2c06-007#5	V2c06-007#5	Fall Time Minimum - 100 ps (mean time)
V2c06-007#14	V2c06-007#14	Total Jitter** (TS1): TP5 - maximum 150 ps
V2c06-016.1.2#5	V2c06-016.1.2#5	Jitter Eye Opening: TP5 - minimum 250 ps
V2c06-016.1.2#5	V2c06-016.1.2#5	Voltage Eye Height: TP5 - minimum 0.89V

** at board side of cable connector or backplane (TP5/TP1)